

***Amendments to the Claims***

This listing of claims will replace all prior versions, and listings of claims in the application.

1. (original) A circuit adapted to be responsive to an input current supplied to an input terminal of the circuit, comprising:
  - an input stage adapted to establish a first bias voltage at the input terminal in response to the input current;
  - a current stage adapted to produce at least one mirror current proportional to the input current in response to the first bias voltage and a second bias voltage;
  - a feedback stage adapted to produce a feedback current proportional to the input current in response to the at least one mirror current; and
  - a reference bias stage adapted to establish the second bias voltage in response to the feedback current from the feedback stage, whereby the first and second bias voltages track the input current.

Claims 2-14 (cancelled)